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T.M. beta-Brown 10/1/03
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.08/852,158
Filing Date May 6, 1997
Inventor..... Sharad Mathur et al.
Group Art Unit2151
Examiner S.X. Lao
Attorney's Docket No. MS1-151US
Confirmation No.....6705
Title: Controlling Memory Usage in Systems Having Limited Physical Memory

RESPONSE TO OFFICE ACTIONS DATED APRIL 4 AND 22, AND MAY 27, 2003

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To: Commissioner for Patents,
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Technology Center 2100

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AMENDMENTS

Amendments to the claims begin on page 2 of this paper.

New claims begin on page 16 of this paper.

Remarks/Arguments begin on page 20 of this paper.

Allowable subject matter, and traverse of rejection under 35 U.S.C.

§112, 2ND ¶, begins on page 21 of this paper.

A listing of art rejections begins on page 22 of this paper.

Traverse of rejection under 35 U.S.C. §102 begins on page 23 of this paper.

Traverse of rejection under 35 U.S.C. §103 begins on page 29 of this paper.

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02 FC:1201 504.00 DA